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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/732,085	12/06/2000	Daniel J. Miller	MS1-642US	3091

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EXAMINER

TRUONG, LECHI

ART UNIT	PAPER NUMBER
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2194

DATE MAILED: 06/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/732,085

Applicant(s)

MILLER ET AL.

Examiner

LeChi Truong

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 January 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-33 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-33 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 1-33 are presented for the examination.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims **1-22 and 29-33** are rejected under 35 U.S.C. 103(a) as being unpatentable over Amini et al (US. Patent 6,581,102 B1) in view of Zenchelsky et al (6,173,364 B1).

3. **As to claim 1**, Amini teaches the invention substantially as claimed including:
one or more processing chains (stream processing modules, col 4, ln 46-54), a development project (controller application, col 4, ln 61-64), a series of filters (filters, col 3, ln 20-22/ col 4, ln 48-50), a render media content (media data, col 4, ln 50-51), loading one or more source processing chains to support execution of a development project (col 6, ln 20-26), and

determining whether each of the one or more processing chains will be subsequently required during execution of this or another development project (col 6, ln 27-33).

4. Amini does not explicit teach caching those filter chains. However, Zenchelsky teaches caching those filter chains (the 5-tuple is then added to the cache as a part of a

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session entry called a session key. The drop action is stored as the action corresponding to the session key, col 4, ln 49-54/ abstract, ln 8-12/ col 9, ln 27-31 and ln 37-40).

5. It would have been obvious to one of the ordinary skill in the art at the time the invention was made to combine the teaching of Amini and Zenchelsky because Zenchelsky's caching those filter chains would improve throughput and efficiency of Amini's system by allowing a cache for a dynamic filter to reduce burden on the filter processor and allowing an efficient way to implement the rules of a dynamic rule without having to search the entire rule base for each packet.

6. **As to claim 2**, Zenchelsky teaches identifying which sources will be required to support execution of the next M seconds of the development project (col 3, ln 59-62), searching one or more caches to determine whether the source processing chains associated with the source are available within the one or more caches (col 4, ln 40-45/ col 7, ln 29-32), retrieving the one or more processing chains from a memory location denoted by an associated one or more pointer in cache (col 8, ln 10-17/ Fig. 4).

7. **As to claim 3**, Zenchelsky teaches determining whether processing chains retrieved from the caches satisfy processing requirements of the development project (col 4, ln 45-44), modifying one or more objects of one or more of the processing chains retrieved from the caches that do not satisfy the processing requirements of the development project (col 5, ln 22-26).

8. **As to claim 4**, Zenchelsky teaches modifying one or more processing object to the processing chains (col 4, ln 54-58).

9. **As to claim 5**, Amini teaches M is at least as long as necessary to construct a processing chain (col 4, ln 39-44/col 7, ln 4-8).

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10. As to claim 6, Zenchelsky teaches determining whether any future calls to a source coupled to the processing chain exist within this development projects and determining whether any future calls to a source coupled to the processing chain may be received during execution of future development projects (col 3, ln 57-62).

11. As to claim 7, Zenchelsky teaches assumed that each processing chain may well be required to support future execution of this or a future development project (col 3, ln 60-62).

12. As to claim 8, Zenchelsky teaches assigning the processing chain a unique identifier (col 5, ln 1-3), storing the unique identifier along with a pointer to a memory location occupied by the processing chain in a cache (col 9, ln 8-25).

13. As to claim 9, Zenchelsky teaches the unique identifier is one or more of a source file handle, a source file name, and a random numeric identifier uniquely (col 8, ln 23-26/ Fig. 4).

14. As to claim 10, it is an apparatus claim of claim 1; therefore, it is rejected for the same reason as claim 1 above.

15. As to claim 11, it is an apparatus claim of claim 1; therefore, it is rejected for the same reason as claim 1 above. In additional, Amini teaches the plurality of executable instruction (col 4, ln 46-48).

16. As to claim 12, it is an apparatus claim of claim 1; therefore, it is rejected for the same reason as claim 1 above. In additional, Amini teaches caching the source chain when it is not current required in the development project (col 4, ln 37-46). The rule of filter was cached before the packet is applied; thus the rules of filters are not currently required before the packet is applied.

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17. As to claim 13, Amini teaches the processing chains are cached only if it will subsequently be required in the development project (col 9, ln 27-30 and ln 35-40).

18. As to claims 14 and 15, they are apparatus claims of claims 8 and 9; therefore, they are rejected for the same reasons as claims 8 and 9 above.

19. As to claim 16, Zenchelsky teaches identifying a need for a source processing chain, retrieving a suitable processing chain from a cache of such processing chain (col 10, ln 54-64).

20. As to claim 17, Amini teaches intergrading the retrieved processing chain into the development project (col 6, ln 19-25).

21. As to claims 18, 19, 20 and 21, they are apparatus claims of claims 3, 5, 10 and 11; therefore, they are rejected for the same reasons as claims 3, 5, 10 and 11 above.

22. As to claim 22, Amini teaches a plurality of source (processing modules, col 4, ln 44-50), an interface (the graph manager 210, col 6, ln 20-23/ Fig. 2), a development project (a controller application, col 4, ln 61-64), processing chains (filters, col 5, ln 54-57), an interface, selectively couple to the plurality of source, to generate and implement a development project of processing chains (col 6, ln 20-25), wherein the interface loads a processing chain for each of the plurality of media source at a point during the execution of the project when the chain is required(col 6, ln 19-26/ col 7, ln 2-10), a series filters to process and render media content(col 6, ln 30-35).

25. As to claim 29, Zenchelsky teaches removes the identifies chains from the active project and caches the removed chains (col 9, ln 24-30 and ln 36-40).

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26. As to claim 30, Zenchelsky teaches the interface loading processing chains (col 9, ln 26-30), by first searching caches of processing chains for suitable match (col 10, ln 54-63).

27. As to claims 31, 32, 33, they are apparatus claims of claims 16,17, 18 and 19; therefore, they are rejected for the same reasons as claims 16, 17, 18 and 19 above.

28. Claims 23-25, 26-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Amini et al (US. Patent 6,581,102 B1) in view of Zenchelsky et al (6,173,364 B1) , as applied to claim 1 above, and further in view of Lewis et al (US. Patent 6,255,943 B1).

29. As to claim 23, Amni and Zenchelsky do not teach loads those chains required if a current chain-count does not exceed a threshold. However, Lewis teaches loads those chains required if a current chain-count does not exceed a threshold (determining the load distributed policy based on a busyness of at least one of the network management servers, col 17, ln 54-57).

30. It would have been obvious to one of the ordinary skill in the art at the time the invention was made to combine the teaching of Amni, Zenchelsky and Lewis because Lewis's loads those chains required if a current chain-count does not exceed a threshold would increase the reliability of Amni and Zenchelsky's systems by allowing the policy-based filters for network management applications to maximize optimal performance and provide reliability of passing object from a server to a client application.

31. As to claim 24, Lewis teaches M is less than a time required to load a processing chain(col 10, ln 19-21).

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32. As to claim 25, Lewis teaches the interface identifies one or more currently loaded chains that can be unloaded (col 6, ln 60-63).

23. As to claim 26, Zenchelsky teaches identifies one or more currently loaded chains that will not be used during next N seconds to upload (col 5, ln 10-12).

24. As to claims 27 and 28, they are apparatus claims of claims 13 and 14; therefore, they are rejected for the same reasons as claims 13 and 14 above.

Response to the argument:

33. Applicant amendment filed on 9/03/04 has been considered but they are not persuasive:

Applicant argued in substance that :

(1) “ the inappropriate motivation is the office’s own example above, simply too general because it could cover almost any alteration contemplated of Amni/ of these reference”.

34. Examiner respectfully disagreed with Applicant's remarks:

As to the point (1), the presented motivations show how the second and third references can be combined to improve the efficiency and reliability of Amini and zenchelsky’s systems.

35. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within

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TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to LeChi Truong whose telephone number is (571) 272 3767. The examiner can normally be reached on 8 - 5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIP. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIP system, contact the Electronic Business Center (EBC) at 866-217-9197(toll-free).

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LeChi Truong

May 31, 2005

A handwritten signature in cursive script, appearing to read "Sue Lao".

SUE LAO
PRIMARY EXAMINER